

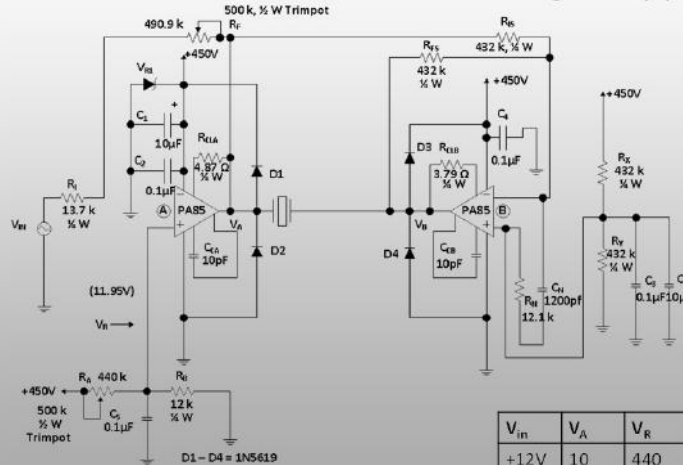


PIEZO DRIVE APPLICATIONS



860 Vpp Piezo Drive

Single Supply Bridge



1. $V_{out} = V_A - V_B$

Max $V_{out} = V_s - V_{SATA} - V_{SATB}$

2. $Gain = V_{out_{p-p}} / V_{in_{p-p}} = (V_A - V_B)_{p-p} / V_{in_{p-p}}$
 $860V_{p-p} / 12V_{p-p} = 71.67$

Gain = 2 R_F / R_I since we have a bridge configuration. That is the voltage gain across the load is twice that of the primary amplifier, A, since +1V out of amplifier A yields -1V out of amplifier B, relative to the mid point power supply reference of +225V.

Therefore $R_F / R_I = 71.67 / 2 = 35.833$

3. Offset:

$$V_A - V_B = V_s \left(2 \left(1 + \frac{R_F}{R_I} \right) \left(\frac{R_B}{R_A} \right) - 1 \right) - 2 \left(\frac{R_F}{R_I} \right) V_{in}$$

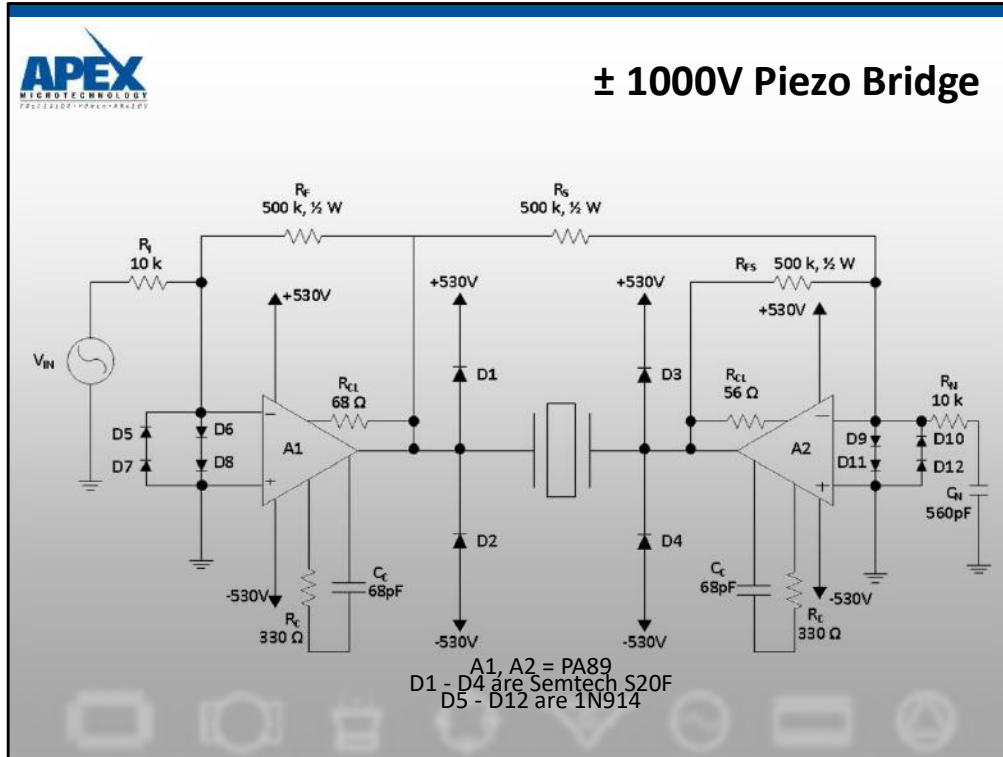
When $V_{in} = 0$ then $V_A - V_B = +430V$

Using $R_F / R_I = 35.833$ and solving above yields $R_A = 36.669R_B$

Choosing $R_B = 12K$ implies $R_A = 440K$

4. Check for common mode voltage compliance: $11.95V > 10V$; OK.

Ref. AN25



Piezo users appear to never have enough voltage. As soon as it was introduced the PA89 found its way into bridge circuits to drive piezos at $\pm 1100\text{V}$ and beyond.

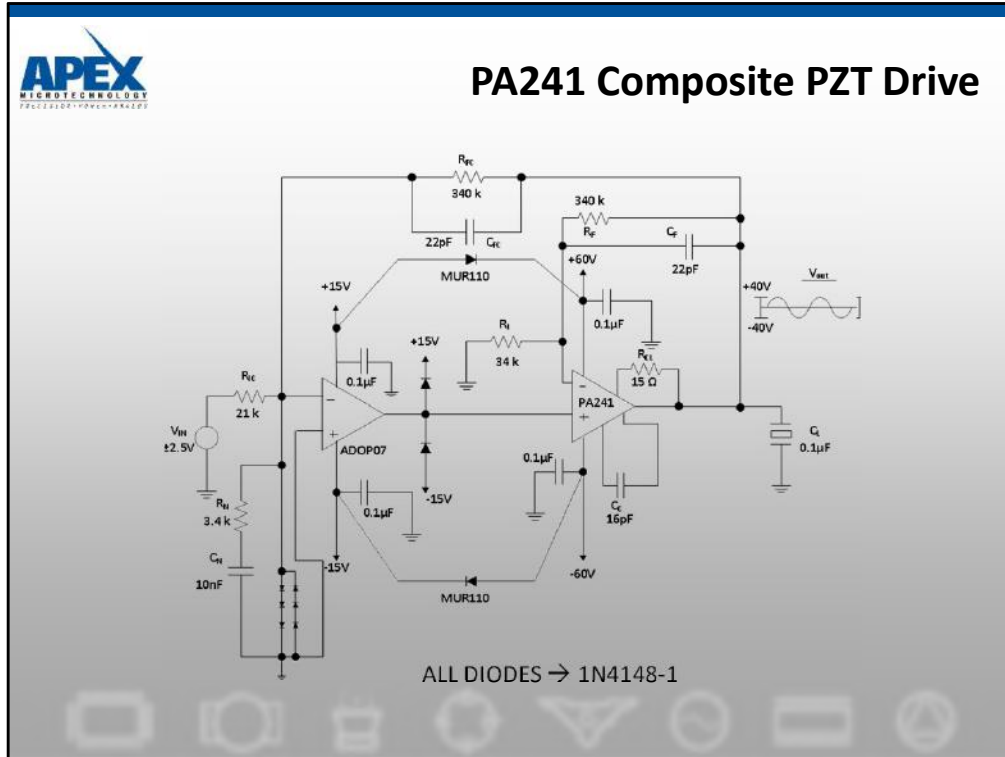
In this application we use the dual supply bridge configuration to deliver up to almost twice the supply voltage of 530V across the load. A1 operates in a gain of 50 to translate the $\pm 10\text{V}$ input to $\pm 500\text{V}$ out of A1. A2 then inverts this output to add an additional $\mp 500\text{V}$ across the Piezo to yield a net $\pm 1000\text{V}$.

A2 uses noise gain compensation to allow its V_o/V_{in} transfer function to remain at -1 , though its compensation capacitor C_c is set for a gain of 50. The noise gain will allow AC stability as well as a balanced bridge since both amplifiers are now compensated identically for the same slew rate.

Input protection diodes, output flyback diodes and proper component selection enhance reliability. Remember to select C_c capacitors with a voltage rating of at least 1100V , R_I , R_F , R_{IS} , and R_{FS} with proper power dissipation and voltage coefficient of resistance, and D1 - D4 with a PIV of at least 1100V .

As a final note remember to check the amplifiers for AC stability due to capacitive loading depending upon the capacitance of the piezo being driven.

Ref. AN25



This circuit is included as an example in Power Design.xls. It is different from most power op amps in that current limit from positive side to negative side does not match well at all.

We will start by stabilizing the power stage, then the composite. Then we will examine current limit and frequency limitations imposed by this current limit.

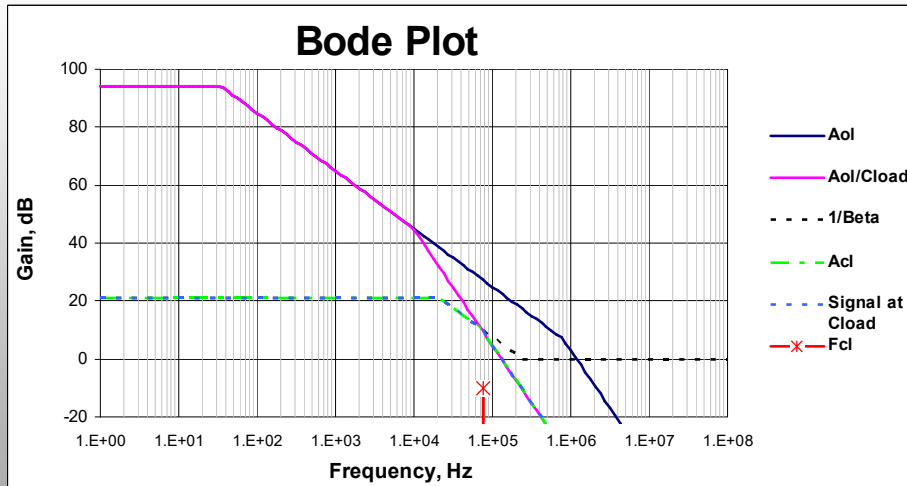
1N4148 diodes on the input of the OP07 provide differential and common mode over voltage protection for transients through C_{fc} . Diodes on the output of the OP07 prevent over voltage transients that can occur through C_f , through the PA241 input protection diodes to the OP07 output through the PA241 internal input protection diodes.

Fast recovery diodes between pairs of supplies ensure that the PA241 input stage is protected from over voltage in the event the $\pm 15V$ supplies are up before the high voltage supplies.

Ref. AN19,AN25



Power Op Amp Magnitude Plot



In any composite amplifier, make sure the power output stage is stable first. Any of the techniques we learned earlier can be used.

Ref. AN19, AN25



Composite Amplifier Set-up

Composite Circuits 38 Goto Main Circuit

MODEL	OP07	READ ME		Estimated Closure Frequency =	56.23413 KHz
Aol =	135 dB	Pole 1 =	0.1 Hz	Suggested maximum bandwidth	5.623413 KHz
Pole 2 =	7.00E+05 Hz	Pole 3 =	7.00E+06 Hz	Estimated Closure Rate =	20.0 dB/decade
Rin	21 Kohms	Rn	3.4 Kohms	Estimated Phase Margin =	50.625 Degrees
Rf	340 Kohms	Cn	10 nF	Load Host Data	
Cf	22 pF	Using Look-Up data			

Notes:

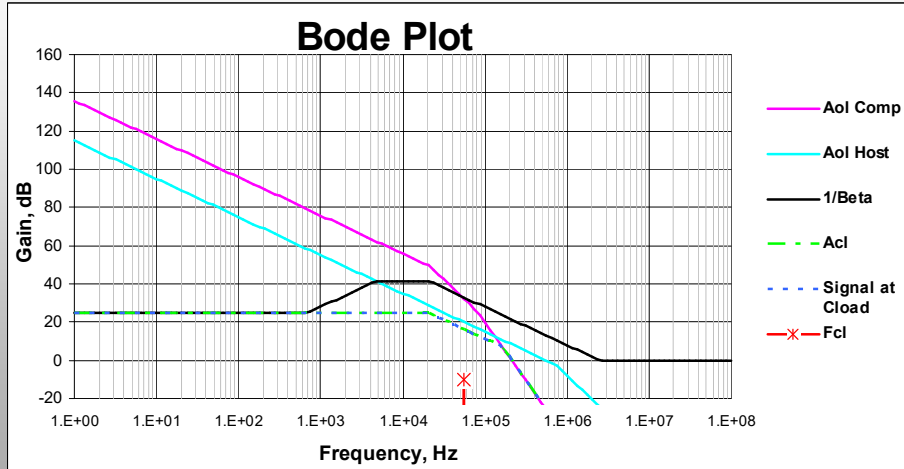
R-C Pole Calculator:	
3.4 Kohms	47 Kohms
5 KHz	10 nF
9.36206 nF	0.338627538 KHz

1/Beta (DC)	24.7 dB	30 Print Data, Bode & Phase	31 Print Data, Bode, Phase & Parts
Noise Gain	16.7 dB		
Pole Noise Gain	4.681027677 KHz		
Zero Noise Gain	0.68665214 KHz		
Pole Cf/Rf	21.27739871 KHz		
Zero Rf/Cf	2493.508487 KHz		

Ref. AN19, AN25, AN38



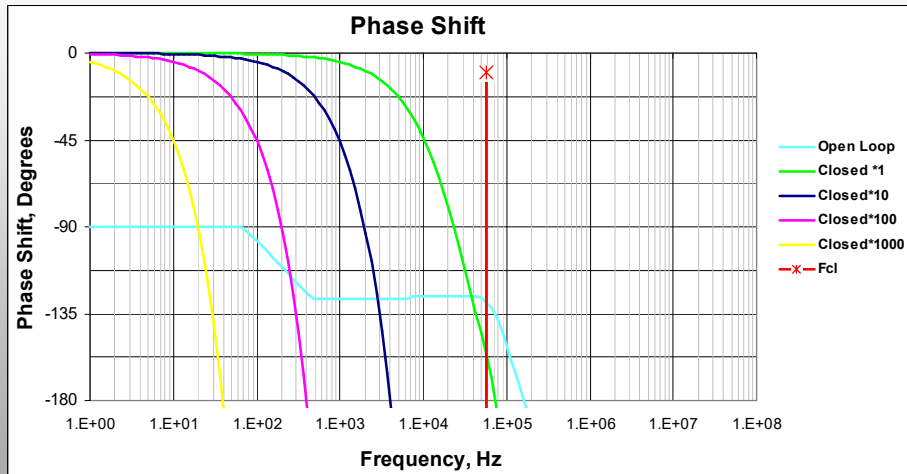
Composite Amplifier Magnitude Plot



Ref. AN19, AN25, AN38



Composite Amplifier Open Loop Phase Plot

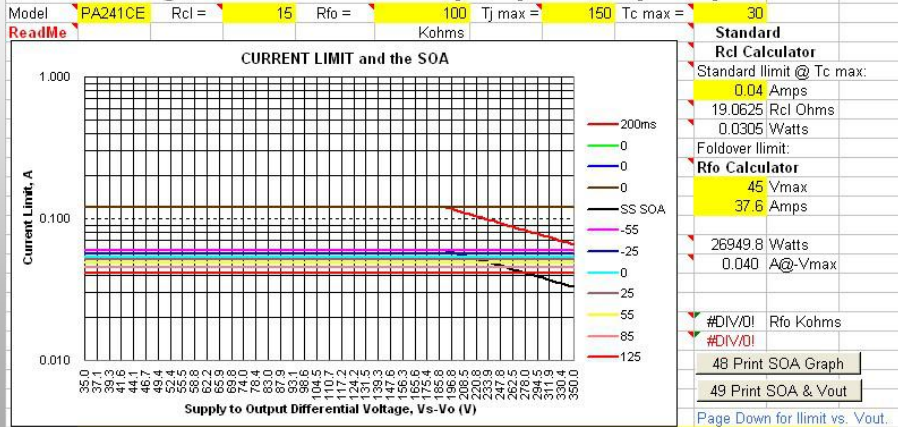


Ref. AN19, AN25, AN38



PA241 Current Limit

Calculating current limit for Apex power op amps



The amplifier selection, load and voltages have all been given. The only frequency that matters is the maximum (no current into a C load at DC). Our stability analysis suggested a maximum of about 10KHz (the Rf-Cf pole frequency).

Ref. AN37



PA241 Power Question Setup

Calculating Power Dissipation for Apex power op amps							
Model	PA241CE	Ta max =	30	Tj max =	150	Tc max =	85
Power for Sine Wave Outputs				Note/PA46			
Vs	60	Volts	Note/PA21_5_6				
Fmin	0.01	KHz	Note/vboost				
Fmax	5	KHz	Bridge ckt?				
Sig	40	Units	No				
Sig as ?	V peak	Note/W					
Res	0	Ohms	# of Amps in parallel?				
Cap	0.1	uF	1				
Ind	0	mH					
Rcap	19	Ohms	Unipolar or Bipolar?				
Rind	15	Ohms	Bipolar				
Piq	0.2484	Watts		32Results	33Results	34Results	35Results
Read Me							
Resonant Frequency =	503292.12	KHz		ERRORS?	Max delta Tj =	120	37Define
At Fmax:					Max delta Tc =	55	36Results
Xc hi =	318.30989	Xc lo =	159154.94				
Xl hi =	3.142E-08	Xl lo =	6.283E-11				
				65 View Last Frequency Sweep			

The amplifier selection, load and voltages have all been given. The only frequency that matters is the maximum (no current into a C load at DC). Our stability analysis suggested a maximum of about 10KHz (the Rf-Cf pole frequency).

Ref. AN37



Speed is Limited by Iout

	At Fmin:	At Fmax:		At Fmin:	At Fmax:		
Z in Ohms	159154.94	318.30989		Maximum AC Pint			
Phase angle	-90.00	-90.00		60	60	Vpk	
RMS Amperes	0.0001777	0.0888577		42.426407	42.426407	Vrms	
Peak Amperes	0.0002513	0.1256637		0.0002666	0.1332865	Arms	
RMS Volts	28.284271	28.284271		0.0113097	5.6548668	Wrms	
Peak Volts	40	40		7.108E-15	1.777E-09	Wtrue	
RMS Power	0.0050265	2.5132741		0.0143997	7.1998695	Pin	
Peak Power	0.0100531	5.0265482		Minimum HS:		9.89	°C/W
Power factor	0.000	0.000		Actual HS:		5.2	°C/W
Input power	0.01	4.80		Results in Tjmax =		92.68	°C
True power	0.00	0.00		Results in Tcmax =		61.2995407	°C
Percent Efficiency =	1.95	49.78					
Vpk capability =	51.65	40.23					
Op amp internal dissipation:							
Input power	0.01	4.80					
Dissipation RMS	0.0095998	4.80					
Dissipation Peak	0.02	8.76					
Total in heatsink	0.27	5.05					
WC watts & Rth	5.048313	6.5					
Readme							

*Back to Previous Section Header

255mA would be required to drive the .1μF load at 10Kz! Notice the “CURRENT TOO HIGH!” flag at the lower right. This is based on data sheet maximum, not the current limit resistor used. Since this is 10x our capability, 1KHz will be the limit with a 75Ω current limit resistor. When this is plugged in, we will find normal operation with no heatsink is possible. To analyze fault conditions, find the lowest impedance to be encountered, assume the current limit (47mA in this case) is driven into the load and calculate the output voltage. Subtract this from the supply voltage, compare to the SOA of the amplifier and calculate a larger heatsink as required.

Ref. AN37