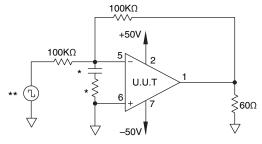


Table 4 Group A Inspection

SG	PARAMETER	SYMBOL	TEMP.	POWER	TEST CONDITIONS	MIN	MAX	UNITS
1 1 1 1 1 1	Quiescent Current Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input Bias Current, -IN Input Offset Current		25°C 25°C 25°C 25°C 25°C 25°C	±150V ±150V ±15V ±150V ±150V ±150V	$V_{IN} = 0, A_{V} = 100$ $V_{IN} = 0, A_{V} = 100$ $V_{IN} = 0, A_{V} = 100$ $V_{IN} = 0$ $V_{IN} = 0$ $V_{IN} = 0$		8.5 3 5.7 50 50 50	mA mV mV pA pA pA
3 3 3 3 3	Quiescent Current Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input BiasCurrent, -IN Input Offset Current		-55°C -55°C -55°C -55°C -55°C -55°C	±150V ±150V ±15V ±150V ±150V ±150V	$V_{IN} = 0, A_{V} = 100$ $V_{IN} = 0, A_{V} = 100$ $V_{IN} = 0, A_{V} = 100$ $V_{IN} = 0$ $V_{IN} = 0$ $V_{IN} = 0$		10 5 7.7 50 50 50	mA mV mV pA pA pA
2 2 2 2 2 2	Quiescent Current Input Offset Voltage Input Offset Voltage Input Bias Current, +IN Input Bias Current, -IN Input Offset Current		125°C 125°C 125°C 125°C 125°C 125°C	±150V ±150V ±15V ±150V ±150V ±150V	$\begin{aligned} &V_{IN} = 0, A_{V} = 100 \\ &V_{IN} = 0, A_{V} = 100 \\ &V_{IN} = 0, A_{V} = 100 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \\ &V_{IN} = 0 \end{aligned}$		10 5.5 8.2 10 10	mA mV mV nA nA
4 4 4 4 4 4	Output Voltage, I _o = 75mA Output Voltage, I _o = 29mA Current Limits Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V _o V _o I _{CL} E _N SR A _{oL} CMR	25°C 25°C 25°C 25°C 25°C 25°C 25°C	±85V ±150V ±30V ±150V ±150V ±150V ±32.5V	$\begin{aligned} R_{L} &= 1 K \\ R_{L} &= 5 K \\ R_{L} &= 100 \Omega \\ R_{L} &= 5 K, A_{V} = 1, C_{L} = 10 n F \\ R_{L} &= 5 K \\ R_{L} &= 5 K, F = 10 Hz \\ R_{L} &= 5 K, F = D C, V_{CM} = \pm 22.5 V \end{aligned}$	75 145 75 20 96 90	125 1 80	V V mA mV V/µs dB dB
6 6 6 6 6	Output Voltage, I _o = 40mA Output Voltage, I _o = 29mA Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V _o V _o E _N SR A _{oL} CMR	-55°C -55°C -55°C -55°C -55°C -55°C	±45V ±150V ±150V ±150V ±150V ±32.5V	$\begin{aligned} R_{L} &= 1 \text{K} \\ R_{L} &= 5 \text{K} \\ R_{L} &= 5 \text{K}, A_{V} = 1, C_{L} = 10 \text{nF} \\ R_{L} &= 5 \text{K}, F = 5 \text{Hz} \\ R_{L} &= 5 \text{K}, F = 5 \text{C}, V_{CM} = \pm 22.5 \text{V} \end{aligned}$	40 145 20 96 90	1 80	V V mV V/µs dB dB
5 5 5 5 5 5	Output Voltage, I _o = 40mA Output Voltage, I _o = 29mA Stability/Noise Slew Rate Open Loop Gain Common Mode Rejection	V _o V _o E _N SR A _o L CMR	125°C 125°C 125°C 125°C 125°C 125°C	±45V ±150V ±150V ±150V ±150V ±32.5V	$\begin{aligned} R_{L} &= 1 K \\ R_{L} &= 5 K \\ R_{L} &= 5 K, A_{V} = 1, C_{L} = 10 n F \\ R_{L} &= 5 K \\ R_{L} &= 5 K, F = 10 Hz \\ R_{L} &= 5 K, F = D C, V_{CM} = \pm 22.5 V \end{aligned}$	40 145 20 96 90	1 80	V V mV V/µs dB dB

BURN IN CIRCUIT



- These components are used to stabilize device due to poor high frequency characteristics of burn in board.
- Input signals are calculated to result in internal power dissipation of approximately 2.1W at case temperature = 125°C.



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